

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	147810	surface NEAR mount\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:49
L2	1039	surface NEAR mount\$3 WITH inductor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:50
L3	14047	aperture WITH core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 11:03
L4	10454	solder\$3 NEAR1 pad	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:59
L5	88192	wire SAME core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:51
L6	3	2 AND 3 AND 4 AND 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:58
L7	3	2 AND 3 AND 4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:58
L8	6	2 and 3 and 5 and solder\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 10:59
L9	77	aperture WITH core WITH solder\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 11:03
L11	232912	pcb (printed NEAR circuit NEAR board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 12:14
L12	165	11 AND 3 AND 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:02
L13	20352	insulat\$3 WITH core WITH (wire coil)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:04

L14	58	2 AND 13	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/18 13:04
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